

FIG.1A

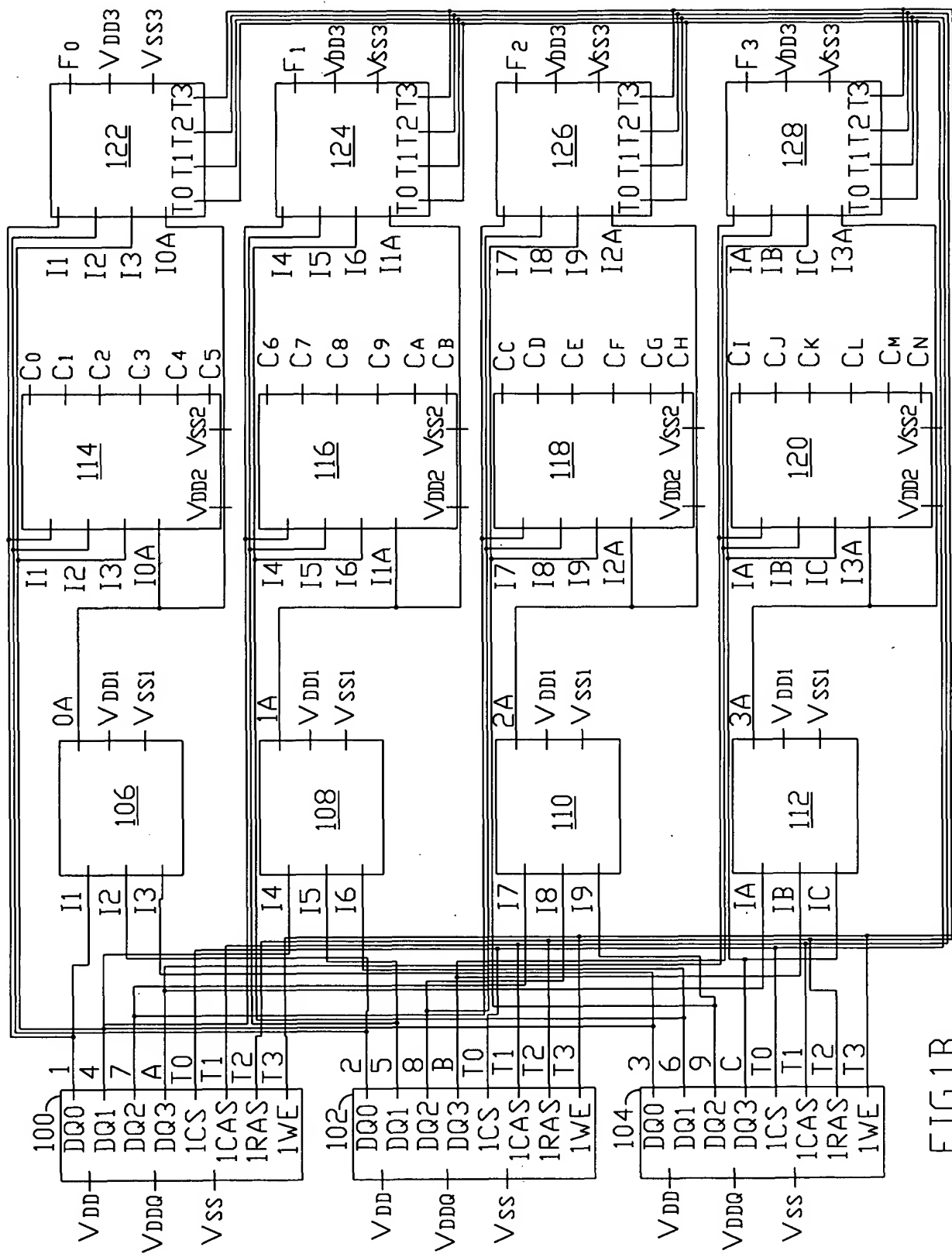


FIG.1B

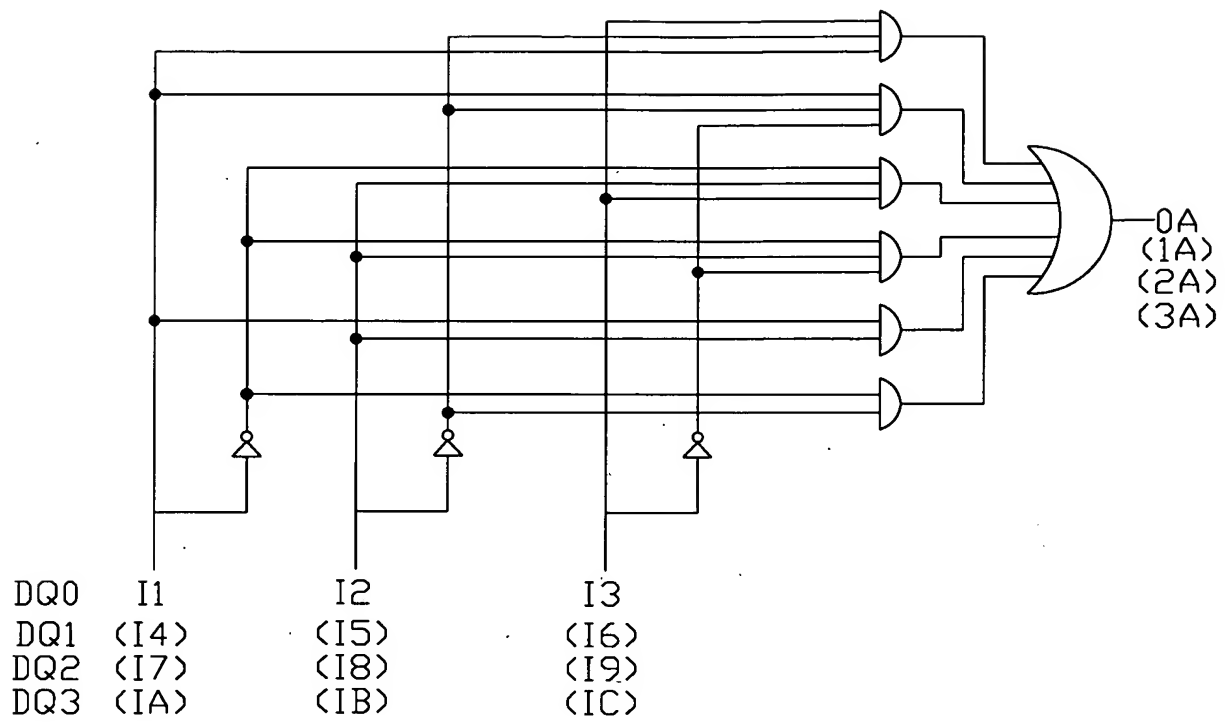


FIG.2A

Input			Output
I1(I4,I7,IA)	I2(I5,I8,IB)	I3(I6,I9,IC)	0A(1A,2A,3A)
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

FIG.2B

Output						Corresponding results
C <sub>0</sub>	C <sub>1</sub>	C <sub>2</sub>	C <sub>3</sub>	C <sub>4</sub>	C <sub>5</sub>	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I2(second memory)occurs error
1	1	1	0	0	0	I1(first memory)occurs error
0	1	0	1	0	1	I3(third memory)occurs error
0	0	1	0	1	1	0A(first compare circuit) occurs error

FIG.3A

Output						Corresponding results
C <sub>6</sub>	C <sub>7</sub>	C <sub>8</sub>	C <sub>9</sub>	C <sub>A</sub>	C <sub>B</sub>	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I5(second memory)occurs error
1	1	1	0	0	0	I4(first memory)occurs error
0	1	0	1	0	1	I6(third memory)occurs error
0	0	1	0	1	1	1A(second compare circuit) occurs error

FIG.3B

Output						Corresponding results
C <sub>C</sub>	C <sub>D</sub>	C <sub>E</sub>	C <sub>F</sub>	C <sub>G</sub>	C <sub>H</sub>	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	I8(second memory)occurs error
1	1	1	0	0	0	I7(first memory)occurs error
0	1	0	1	0	1	I9(third memory)occurs error
0	0	1	0	1	1	2A(third compare circuit) occurs error

FIG.3C

Output						Corresponding results
C <sub>I</sub>	C <sub>J</sub>	C <sub>K</sub>	C <sub>L</sub>	C <sub>M</sub>	C <sub>N</sub>	
0	0	0	0	0	0	Normal
1	0	0	1	1	0	IB(second memory)occurs error
1	1	1	0	0	0	IA(first memory)occurs error
0	1	0	1	0	1	IC(third memory)occurs error
0	0	1	0	1	1	3A(fourth compare circuit) occurs error

FIG.3D

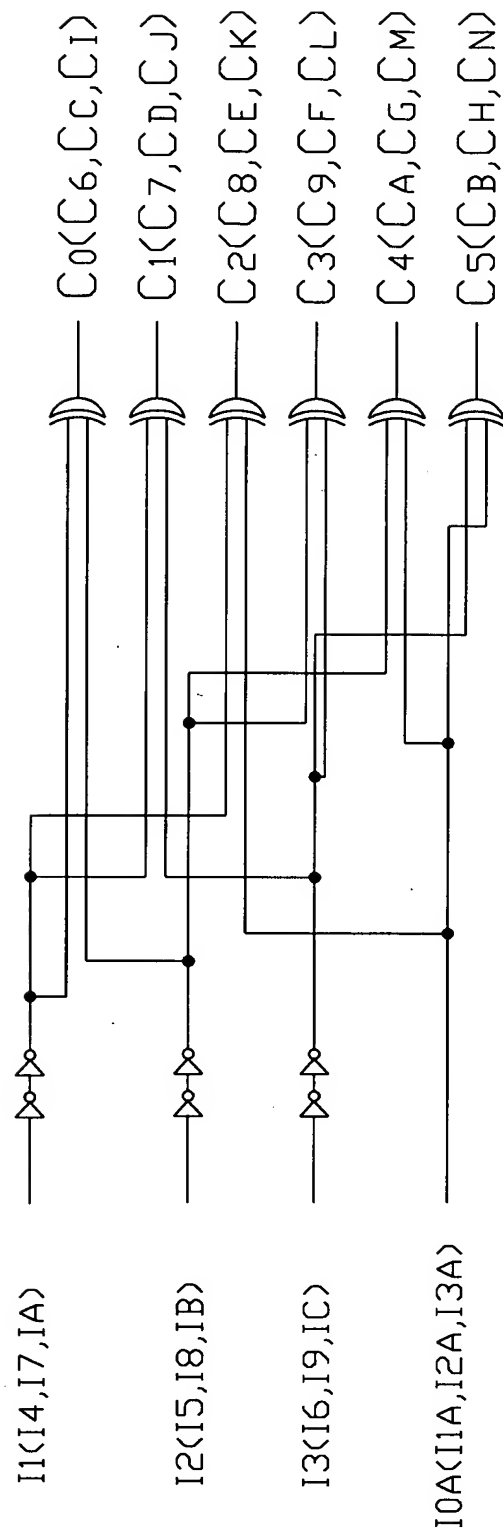


FIG.4

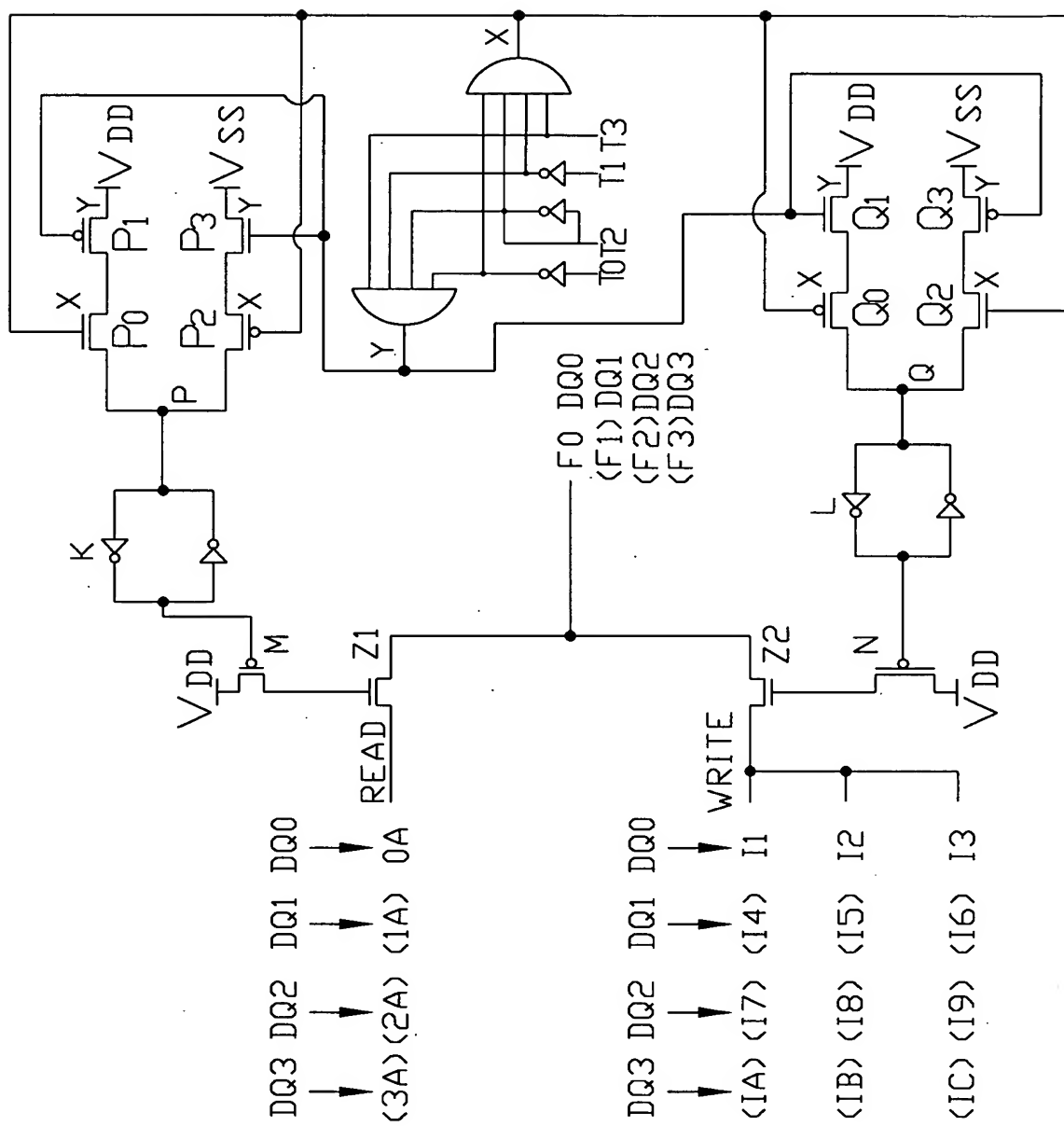


FIG. 5

FIG.6  
FIG.6A  
FIG.6B

X	Y	X'	Y'	X''	Y''	P	Q	left K	left L	Memory read/write
0	0	—	—	—	—	—	—	—	—	No any Read/Write
0	1	—	—	—	—	0	1	1	0	Write
0	0	0	1	—	—	0	1	1	0	After Write, X=0 Y=0 turned up
0	0	0	0	0	1	0	1	1	0	After Write,hold X=0 Y=0
1	0	0	1	—	—	1	0	0	1	After Write,Read turned up immediately
0	0	1	0	0	1	1	0	0	1	After Write,Read turned up immediately and then X=0 Y=0 turned up
0	1	1	0	0	1	0	1	1	0	After Write,Read turned up and then Write turned up
1	0	1	0	0	1	1	0	0	1	After Write,hold Read
0	1	0	1	—	—	0	1	1	0	After Write,Write turned up again
0	0	0	1	0	1	0	1	1	0	After Write,Write turned up again and then X=0 Y=0 turned up
1	0	0	1	0	1	1	0	0	1	After Write,Write turned up again and then Read turned up
1	0	—	—	—	—	1	0	0	1	Read
0	0	1	0	—	—	1	0	0	1	After Read, X=0 Y=0 turned up
0	0	0	0	1	0	1	0	0	1	After Read,hold X=0 Y=0
0	1	1	0	—	—	0	1	1	0	After Read,Write turned up

FIG.6A



0	0	0	1	1	0	0	1	1	0	0	After Read, Write turned up immediately and then X=0 Y=0 turned up
0	1	0	1	1	0	0	1	1	0	0	After Read, hold Write
1	0	0	1	1	0	1	0	0	1	1	After Read, Write turned up and then Read turned up
1	0	1	0	—	—	1	0	0	1	1	After Read, hold Read
0	0	1	0	1	0	1	0	0	1	1	After Read, Read turned up again and then X=0 Y=0 turned up
0	1	1	0	1	0	0	1	1	0	0	After Read, Read turned up again and then Write turned up
0	1	0	0	1	0	0	1	1	0	0	After Read, X=0 Y=0 turned up and then Write turned up
1	0	0	0	0	1	1	0	0	1	1	After Read, X=0 Y=0 turned up and then Read turned up

PS:

$$X = X(t)$$

$$Y = Y(t)$$

$$X' = X(t-1)$$

$$Y' = Y(t-1)$$

$$X'' = X(t-2)$$

$$Y'' = Y(t-2)$$

FIG.6B